

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **NAKAYAMA, Akitaka, et al.**

Group Art Unit: 3653

Serial No.: 09/715,081

Examiner: Jeffery A. Shapiro

Filed: November 20, 2000

P.T.O. Confirmation No.: 7823

For: **PRODUCTION SYSTEM FOR PRINTED WIRING BOARD**

RESPONSE UNDER 37 CFR §1.116

- EXPEDITED RESPONSE -

GROUP ART UNIT 3653

BOX AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DRAFT

Sir:

In response to the Office Action dated February 28, 2003, please amend the above-identified application as follows:

IN THE CLAIMS:

Amend claims 1 and 9 as indicated below:

1. (Twice Amended) A manufacturing system for manufacturing printed wiring boards of plural types, said printed wiring boards scheduled to be manufactured are laid out on at least one predetermined manufacturing block, comprising:

a schedule data storage unit storing manufacturing schedule data including printed wiring board data including each type of the printed wiring boards and the number of each of the printed wiring boards scheduled to be manufactured;

a detecting unit detecting a fractional printed wiring board which should be laid out to a